



Digital Logging PCIe Boards for HMP



These digital logging boards provide up to two T1/E1 digital network interfaces utilizing PIKA's host based media processing (HMP-X) engine for all operations. Highly flexible, they are software-configurable for use in multiple countries.

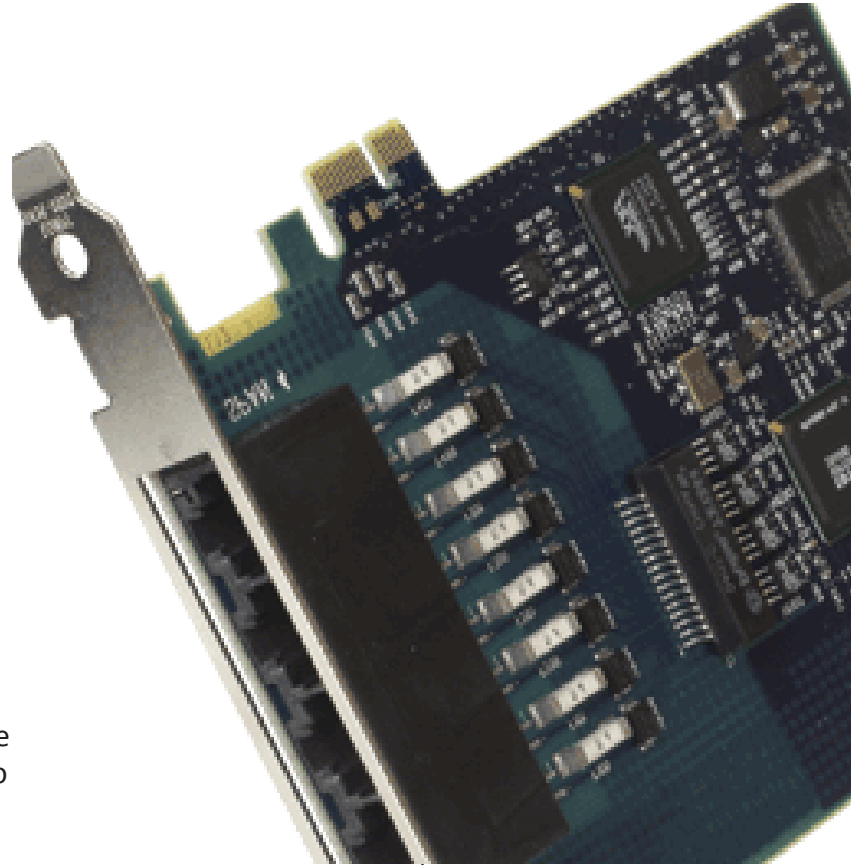
This 6 inch board features the PCI Express format, and will easily integrate into a standard PC chassis. Media processing is performed using PIKA's host-based (HMP) software resources.

Digital board densities are available to tap one or two spans. (Two connectors are required to tap one span of conversation.)

Cloud based logging together with encryption and compression is available via Pika's high level API.

Key Features

- Connects to FXO, FXS and 4-wire analog lines
- 1 to 2 span T1/E1 connectivity to deliver superior densities
- Integrated support of PIKA Technologies' Host Media Processing (HMP-X) architecture
- Integrated support of the low-level or high-level PIKA API delivers maximum design control and application performance
- ISDN (CCS) and RBS/MFR2 (CAS) protocols
- Two connectors are required to tap one span
- Windows or Linux development environments
- One board support for 60 channels
- Voice activity detection (VAD), time stamping and audio compression
- Support for uLaw, aLaw, GSM, AMR, audio formats
- CDR details
- Native Logging API with Cloud support
- Environments via PIKA's low and high level APIs
- C, C++ logging API with RESTful API for Cloud storage.



Technical Specifications

Network Connection	Rear panel RJ48 connectors (DSX-1 or DS-1 interface)
Number of T1/E1 Spans	1 to 2 spans
Protocol	ISDN, Clear Channel, CAS/RBS
Impedance compatibility	T1: 100 ohm, E1: 120 ohm
Switch Types	4ESS, DMS 100, 5ESS, NI-2, NET5
Approvals	North America, European Union
HDLC	3 available per span
Framing	T1: ESF, SF/D4 E1: CRC4, Basic
Encoding	T1: B8ZS, AMI (NONE, GTE, BELL, JB8) E1: HDB3, AMI
Bus Interface: PCIe	Based on PCIe revision 1.0a specifications
PCIe bus speed	2.5 GHz, single lane link
Memory Address Allocation	Automatically assigned by Plug and Play cycle
Interrupts Allocation	Automatically assigned by Plug and Play cycle
Dimensions: PCIe (Metric)	151.8 mm L x 106.7 mm H x 15.2 mm D
PCIe (Imperial)	5.975" L x 4.2" H x 0.6" D
Power Requirements PCIe	Power consumption from 3.3V rail 1 W max
Environment Requirements	Operating Temperature 0 °C to +60 °C
Storage Temperature	-20 °C to +85 °C
Humidity, Non-condensing	5% to 95%
Mean Time Between Failure (MTBF)	101 years

All PIKA boards are RoHS compliant.

PIKA provides a 1-year warranty on all boards.

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